From: chantal dumont
To: satarupa bhattacharya
CC: Les Cundall
Sent: 5/17/2013 6:45:08 PM
Subject: Fwd: RE: Re: Fwd: Re: +++UNIVERSITY OFFER APPROVAL REQUEST FOR [redacted]

Hey Sat,

I returned that offer to you as I cannot put it through in good conscience with that number.

Les, please handle or let me know f you need my help. I'd like to see that offer at least in the neighborhood of [redacted]

Thanks,

Chantal

On 5/16/2013 3:40 PM, satarupa bhattacharya wrote:
> Fowler Offer for Austin office.
> I discussed salary range at length with manager but he refused to go
> above [redacted] (his first suggestion was [redacted]
> based upon salaries in his team and also candidate's qualifications.
> Doug has discussed this salary with [redacted] and [redacted] has said that he
> will accept at this salary
> thanks,
> Satarupa
>
>
>
> -------- Original Message --------
> Subject: RE: Re: Fwd: Re: +++UNIVERSITY OFFER APPROVAL REQUEST FOR
> [redacted]
> Date: Thu, 16 May 2013 11:22:46 -0700 (PDT)
> From: Phil Bullinger <phil.bullinger@oracle.com>
> To: Satarupa Bhattacharya <satarupa.bhattacharya@oracle.com>
> CC: Scott Tracy <scott.tracy@oracle.com>, Douglas Patrick
> <douglas.patrick@oracle.com>
>
>
>
> I approve.
>
> -- Phil
>
> *From:*satarupa bhattacharya
> *Sent:* Thursday, May 16, 2013 11:58 AM
> *To:* PHIL.BULLINGER
> *Cc:* SCOTT.TRACY; Douglas Patrick
> *Subject:* Fwd: Re: Fwd: Re: +++UNIVERSITY OFFER APPROVAL REQUEST FOR
> [redacted]
>
> Dear Phil,
> Please approve the following offer for [redacted] and return to me as
> soon as possible.
> Thanks,
> Satarupa
>
>
>
> -------- Original Message --------
>
> *Subject: *
>

Exhibit P-184

> Re: Fwd: Re: +++UNIVERSITY OFFER APPROVAL REQUEST FOR [redacted]
>
> *Date: *
>
> Wed, 15 May 2013 16:49:25 -0600
>
> *From: *
>
> Mike Milillo <mike.milillo@oracle.com> <mailto:mike.milillo@oracle.com>
>
> *Organization: *
>
> Oracle
>
> *To: *
>
> satarupa bhattacharya <satarupa.bhattacharya@oracle.com>
> <mailto:satarupa.bhattacharya@oracle.com>
>
> *CC: *
>
> Douglas Patrick <douglas.patrick@oracle.com>
> <mailto:douglas.patrick@oracle.com>
>
> Approved.
>
> On 5/15/13 4:27 PM, satarupa bhattacharya wrote:
>
> Dear Mike,
> Please approve the following offer for [redacted] and return to me as
> soon as possible.
>
> Thanks,
> Satarupa
>
> -------- Original Message --------
>
> *Subject: *
>
> Re: +++UNIVERSITY OFFER APPROVAL REQUEST FOR [redacted]
>
> *Date: *
>
> Wed, 15 May 2013 15:26:25 -0500
>
> *From: *
>
> Douglas Patrick <douglas.patrick@oracle.com>

> <mailto:douglas.patrick@oracle.com>
>
> *Organization: *
>
>
>
> ZFS Storage Appliance, Solaris NFS Development
>
> *To: *
>
>
>
> satarupa.bhattacharya@oracle.com
> <mailto:satarupa.bhattacharya@oracle.com>
>
> Approved. Thanks!!
>
>
>
> Douglas Patrick
>
>
>
> On 05/15/13 14:58, satarupa bhattacharya wrote:
>
> >
>
> >
>
> >
>
> > Dear Doug,
>
> > Please approve the following offer for [redacted] and return to
>
> > me as soon as possible.
>
> >
>
> > Candidate:[redacted]
>
> > Title: Senior Software Engineer - 10530
>
> > Group: Solaris Network File System
>
> > Cost center: AV09
>
> > Location: Austin-Riata (5300 Riata Park Ct)
>
> > Hiring Manager:Douglas Patrick
>
> > Salary: [redacted]
>
> > Sign-on: [redacted]
>
> > Stock: As approved by Larry Lynn
>
> > Intended Start date: Summer 2013
>
> > Relocation Package: Silver for College Recruits
>
> >
>
> > Thanks,
>
> > Satarupa
>
> > _____

> Satarupa Bhattacharya
>
> Recruiting Program Manager
>
> University Development Recruiting
>
> phone: 650.633.5761
>
> fax: 650.633.1184
>
>
>
>
> _____resume_____
>
> [redacted]
>
> [redacted]
>
> [redacted]
>
> [redacted]@yahoo.com <mailto:[redacted]@yahoo.com>
>
>
>
> EDUCATION
>
>
>
> Doctorate of Philosophy in Computer Science, 09/2006 -- 05/2011
>
> GPA: 3.91/4.00
>
>
>
> The University of Texas at San Antonio
>
>
>
> Dissertation: Scheduling for Energy and Reliability Management on
>
> Multiprocessor Real-Time Systems
>
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>
> Bachelor of Science in Computer Science, 09/2001 - 06/2005
>
>
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>
>
> Beijing University of Posts and Telecommunications
>
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>
> HIGHLIGHTS OF QUALIFICATIONS

> > > § Extensive C/C++ development experience on Linux and Windows
> > > § Extensive experience with developing and implementing sophisticated real-time scheduling algorithms
> > > § Extensive experience on performance profiling and optimization for scientific computing applications
> > > § Substantial experience on multi-threaded programming (pthread and OpenMP)
> > > § In-depth knowledge of algorithms, OS, and computer architecture
> > > § Experience with large code base and ability to quickly understand complicated software system and algorithms
> > > § Familiar with fundamental network concepts and protocols (TCP/IP, HTTP, DNS, ARP, NFS) and experience with socket programming
> > >
> > > EXPERTISE
> > > § Real-Time Scheduling Algorithms, High Performance Computing, Parallel Programming, Linear Algebra, Numerical Analysis, Networking Protocols, Socket Programming
> > >
> > > SKILLS
> > > § Languages: C/C++, STL, x86 and PowerPC Assembly, Java, Bash, Perl, HTML, XML
> > >

> > § Operating Systems: Windows, Linux, Solaris, Mac OS
> >
> > § Applications: Latex, Gnuplot, Awk, Sed, SVN, CVS, Xfig, Windows
> > Performance Toolkit, Visio
> >
> > § Developing Tools: Visual Studio, Intel Composer XE 2011, Intel Math
> > Kernel Library, Matlab, Eclipse, NI Labview
> >
> >
> >
> > Experience
> >
> > Software Engineer (07/2011 - Present) Nanometrics, Inc., Austin, TX
> >
> > Work on software (NanoDiffract) for advanced product quality control for
> > semiconductor manufactures
> >
> > § Design and implement various new functionalities in complex
> > multithreaded software (NanoDiffract).
> >
> > § Designed and implemented GUI and algorithms to support new data types
> > of measurement. (C++ and MFC)
> >
> > § Designed and implemented GUI and algorithms to automate the process of
> > determining pre-defined parameters of the model to measure. (C++ and MFC)
> >
> > § Developed algorithm for conducting sensitivity analysis for new data
> > type. (C++ and Matlab)
> >
> > § Designed and implemented the feature to insert a layer or region above
> > current layer or region in the geometry model through the graphical
> > editor. Developed mechanism to ensure the regions that are coupled or
> > aligned to each other still couple or align properly after insertion.
> > (C++ and MFC)

> > § Optimize algorithms to construct the geometry model for modeling
> > complex 3D structures on wafers. (C and C++)
> > § Optimized the computation engine of NanoDiffract which uses sin and
> > cos computation extensively in a five-level loop. Re-wrote the algorithm
> > and moved the sin and cos computation out of the inner-most loop by
> > using temporary buffers to hold the pre-computed sin and cos values or
> > by computing the sin and cos values of iteration i+1 from iteration i.
> > Achieved 5% to 35% performance improvement depending on the bottleneck.
> > (C and Windows Performance Toolkit)
> > § Evaluated and tuned the computation engine of NanoDiffract which
> > extensively uses level2 and level3 BLAS algorithms and LAPACK algorithms
> > on Intel Sandy Bridge processors with 256-bit VPUs. Investigated the
> > speedup gained by using AVX by linking to different versions of Intel
> > Math Kernel Library (MKL) (version 10.1, 10.2, and 10.3). Found the best
> > compilation flags by extensive empirical experiments. (C, Intel AVX, and
> > Intel MKL)
> > § Profiled and discovered performance bottlenecks of NanoDiffract under
> > various use cases using Windows Performance Toolkit.
> > Research Assistant (09/2006 -- 05/2011) The University of Texas at
> > San Antonio
> > Real-Time Systems
> > Worked on NSF funded projects on Real-Time Scheduling Algorithms and
> > Low-Power Reliable Real-Time Systems
> > § Developed a cluster-based real-time scheduling algorithm for
> > multiprocessor systems and implemented it in a simulator to compare the
> > proposed algorithm with existing algorithms on context switches, task

> > migrations, and scheduler invocation time. Results show that the
> > proposed scheduler reduces context switches and migrations by at least
> > 32% and 35% respectively. (C++)
> >
> > § Developed a real-time scheduling algorithm for multiprocessor systems
> > to minimize the power consumption and ensure system reliability.
> > Implemented the algorithm in an event-driven simulator to investigate
> > energy saving and reliability improvement. (C++)
> >
> > § Experimented dynamic CPU frequency adjustments on Triton-270 embedded
> > test bed by modifying the system configuration file which specifies the
> > current running speed.
> >
> > Parallel Computing
> >
> > Parallelized various algorithms using pthread, OpenMP, and MPI
> >
> > § Used pthread to parallelize the simulator for cluster scheduling
> > algorithms for real-time systems. Implemented it as a thread pool, where
> > the schedule for each cluster is a task and an idle thread picks up a
> > new task until all tasks are accomplished. (C)
> >
> > § Used pthread to parallelize blocked double precision general matrix
> > multiply on an 8-core SPARC machine. Observed up to 5.3 times speedup
> > when thread number is 7 comparing to the serial implementation. (C)
> >
> > § Used MPI to parallelize 3-D Monte Carlo Integration on a Sun cluster.
> > Observed that the initialization time on different nodes varies
> > significantly due to the cluster structure, network traffic, and system
> > workload. (C)
> >
> > § Used OpenMP to parallelize 3-D Monte Carlo Integration and achieved up
> > to 7.4 times speedup on an 8-core SPARC machine. (C)
> >

> > High Performance Computing
> >
> > Conducted performance optimization for linear algebra algorithms on x86,
> > PPC, and SPARC machines using memory optimization and instruction level
> > optimization tricks
> >
> > § Optimized single precision general matrix rank update on PowerPC
> > machine by unrolling the loop by 32, software pipelining, and
> > pre-fetching. (Altivec Assembly)
> >
> > § Optimized in-L2-cache single precision general matrix vector multiply
> > with SSE by loop unrolling. Achieved 2592 MFLOPS on a 2.13GHz Intel
> > machine. (x86 Assembly)
> >
> > § Implemented blocked matrix multiply to fit the working set in L1/L2
> > cache. Observed that only applying L1 blocking gave better speedup.
> > Empirically tuned the block size and found a best block size that gave
> > the most speedup. Achieved up to 4 times speedup comparing to the
> > implementation without blocking. (C)
> >
> > § Designed and implemented a program to achieve floating point peak on a
> > 1062MHz UltraSPARC machine. Achieved 89% of floating point peak by
> > unrolling the loop by 12 and software pipelining. (C)
> >
> > Satellite Image Processing Intern Remote
> > Sensing/Geographic Information
> >
> > (05/2006 -- 08/2006) System Lab, Utah State
> > University
> >
> > Worked on NASA funded projects on Satellite Image Processing
> >
> > § Developed an application to extract layers from huge satellite images
> > with size up to 2GB and re-project the extracted layers to user-friendly

> images. (C and Leica ERDAS)

SELECTED PUBLICATIONS

§ A Study of Utilization Bound and Run-Time Overhead for Cluster Scheduling in Multiprocessor Real-Time Systems, with Dakai Zhu and Hakan Aydin, in the Proc. of the 16th IEEE Intl. Conference on Embedded and Real-Time Computing Systems and Applications (RTCSA), Macau SAR, China, Aug. 2010.

§ Power Management for Real-Time Embedded Systems on Block-Partitioned Multicore Platforms, with Dakai Zhu, in the Proc. of the IEEE Intl. Conference on Embedded Software and Systems (ICESS), Chengdu, P.R.China, Jul. 2008; Best Paper Award.

§ Priority-Monotonic Energy Management for Real-Time Systems with Reliability Requirements, with Dakai Zhu and Hakan Aydin, in the Proc. of the IEEE Intl. Conference on Computer Design (ICCD), Lake Tahoe, CA, Oct. 2007.

Affiliations & Honors

§ ACM Member (2009 - Present)

§ IEEE Member (2007 - Present)

§ Southwestern Bell Fellowship (2010)

> > § ICESS Best Paper Award (2008)
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> --
> Mike Milillo, DM
> Sr. Engineering Manager
> Oracle Storage Systems
> Phone: 303-272-7632 (x27632)
> Mobile: 720-243-3671
> Email:mike.milillo@oracle.com <mailto:mike.milillo@oracle.com>